

FIG. 1

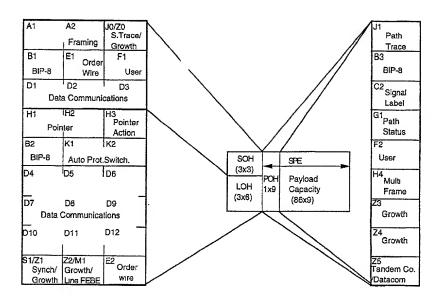
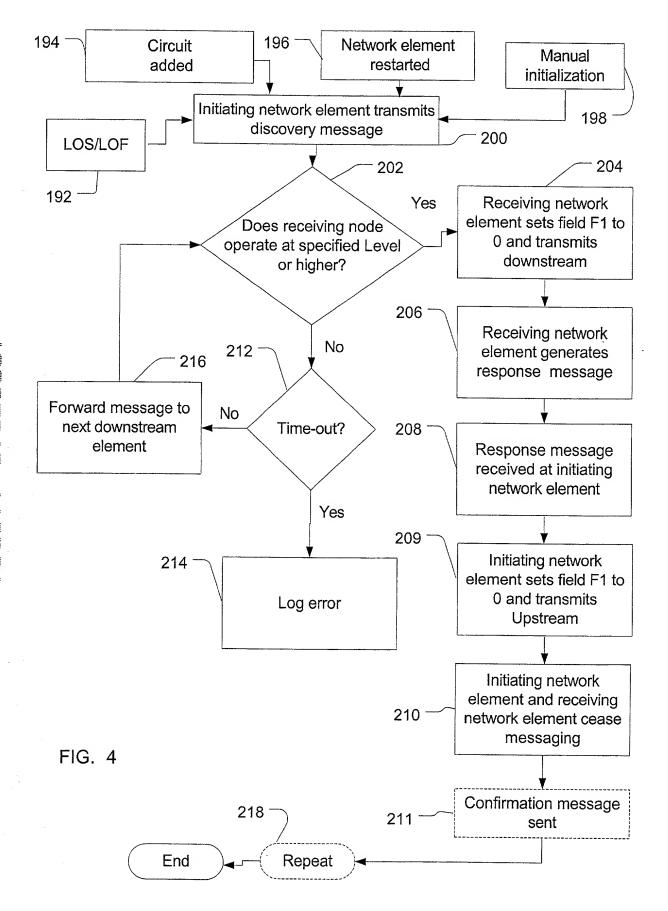


FIG. 2 Conventional Art

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Byte	0		2	3	4	3	9	7	8	6	10	11	12	13	14	15
Label	FS	T	I	×	×	×	×	×	×	×	×	P1	P2	P3	P4	P5
Description	CRC	CRC Level	DisId				Node Identifier	entifier					Por	Port Identifier	ier	
Field	F0	F1	F2				F3							F4		

FIG. 3



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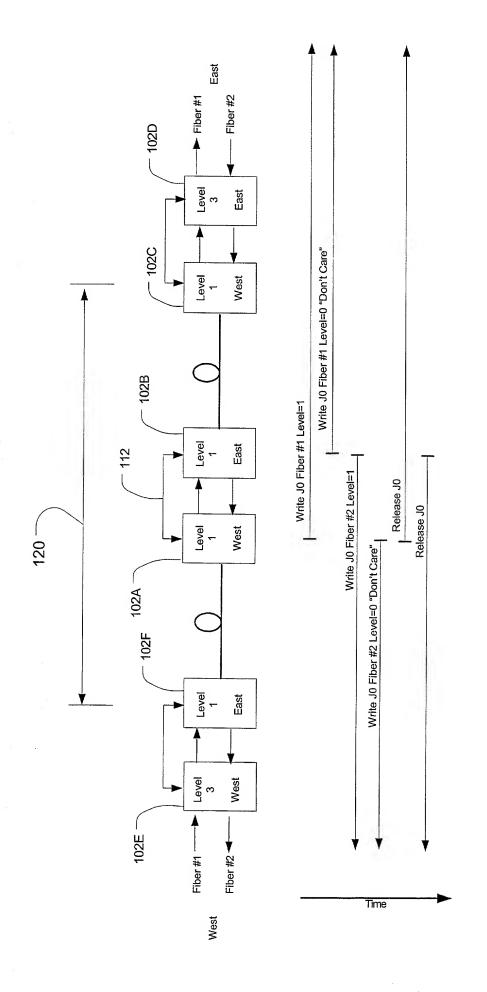


FIG. 5A

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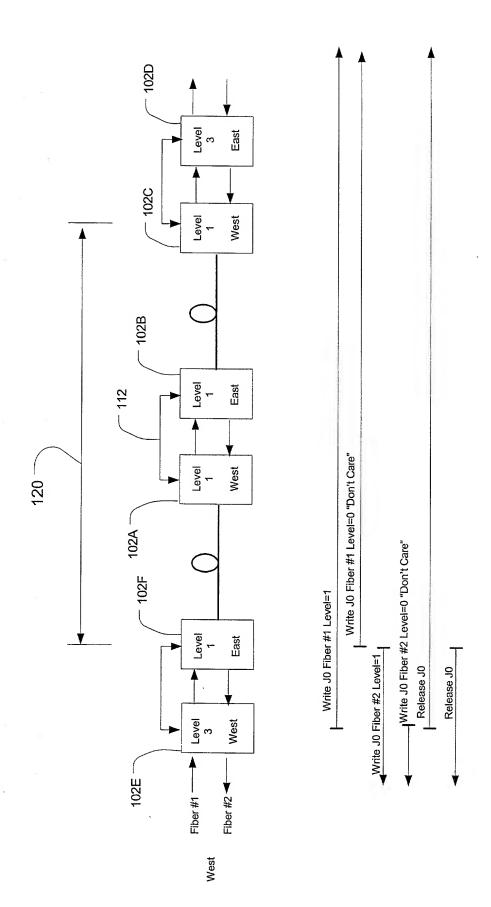


FIG. 5B

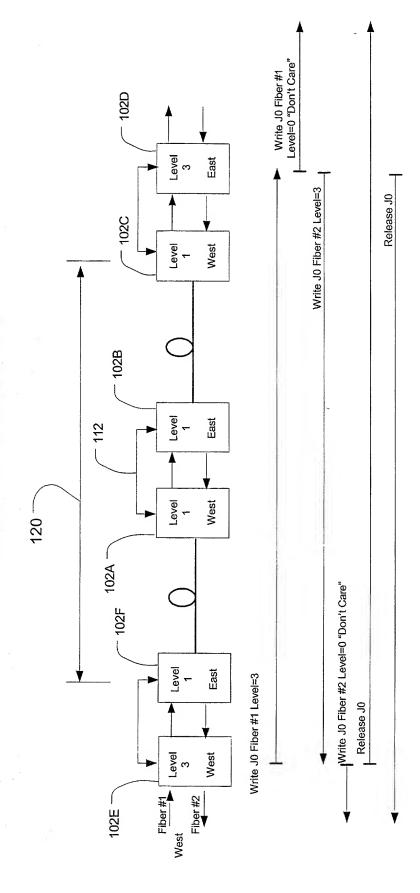
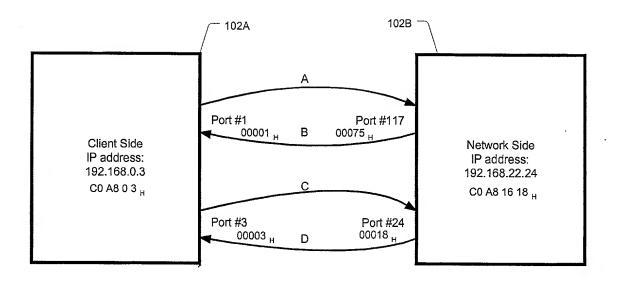
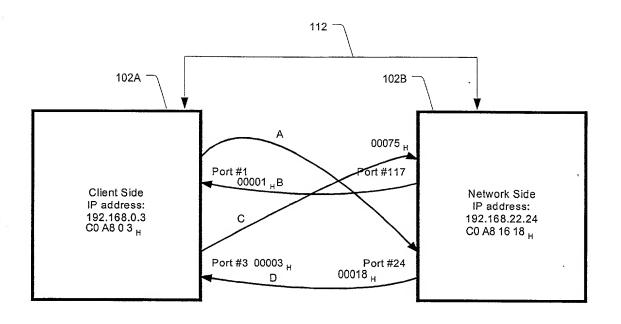


FIG. 5C



- A 1 P C0A80003 00001
- B 1 P C0A81618 00075
- C 1 P C0A80003 00003
- D 1 P C0A81618 00018

FIG. 6



- A 1 P C0A80003 00001
- B 1 P C0A81618 00075
- C 1 P C0A80003 00003
- D 1 P C0A81618 00075

FIG. 7

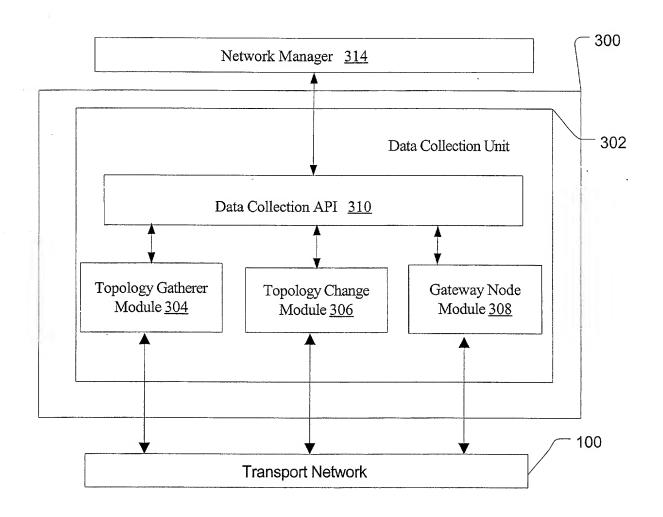


FIG. 8

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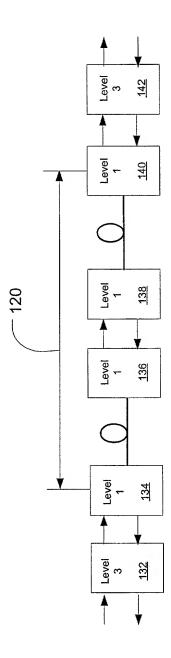
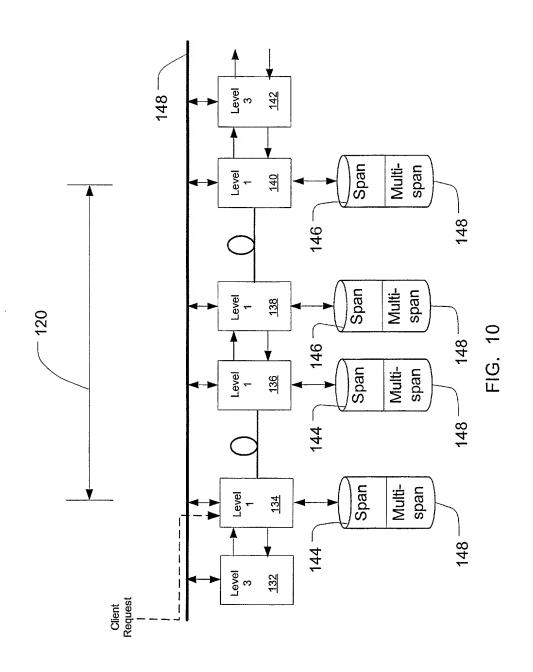
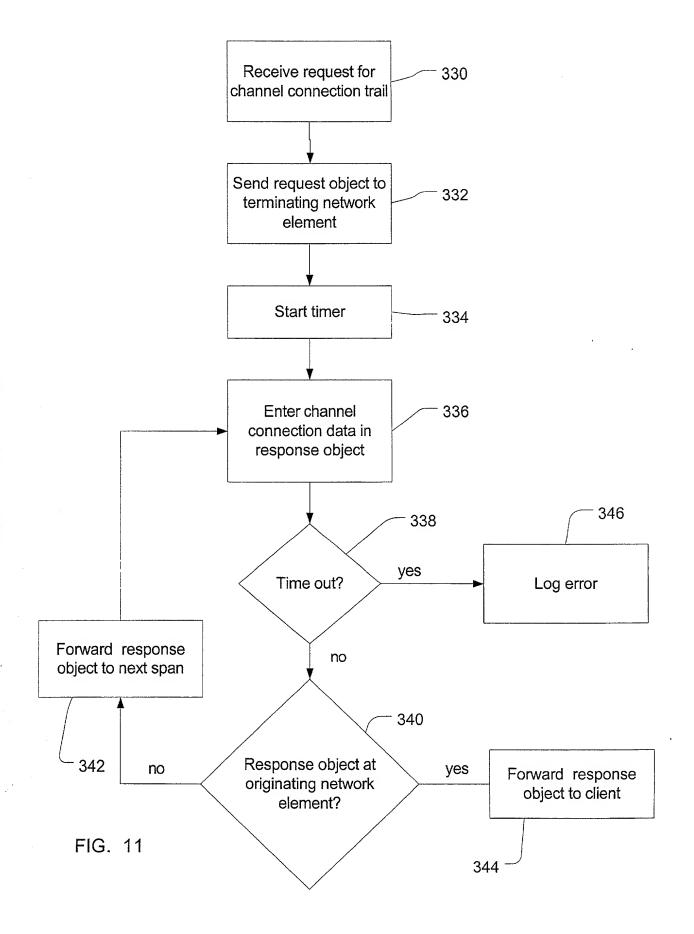


FIG. 9

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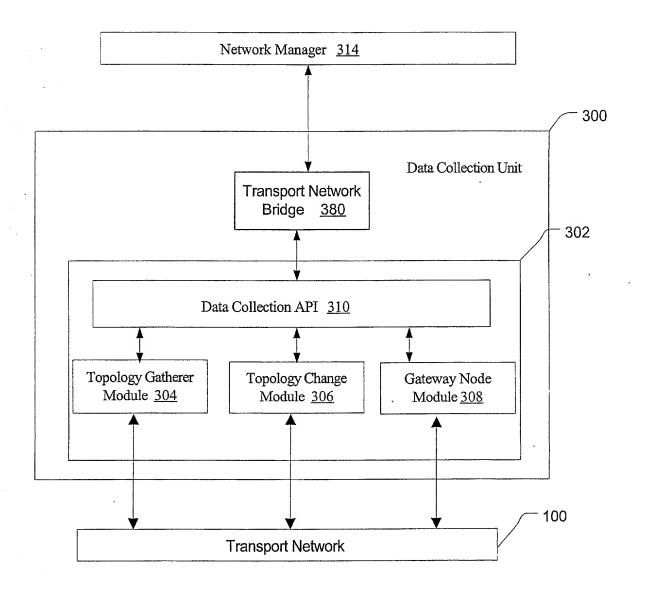


FIG. 12

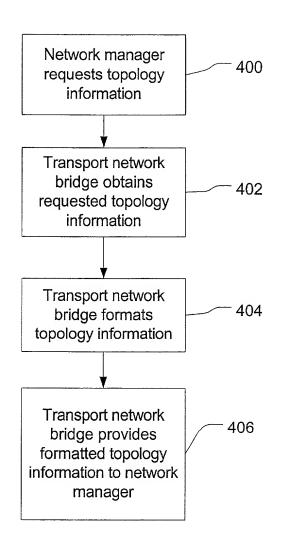


FIG. 13

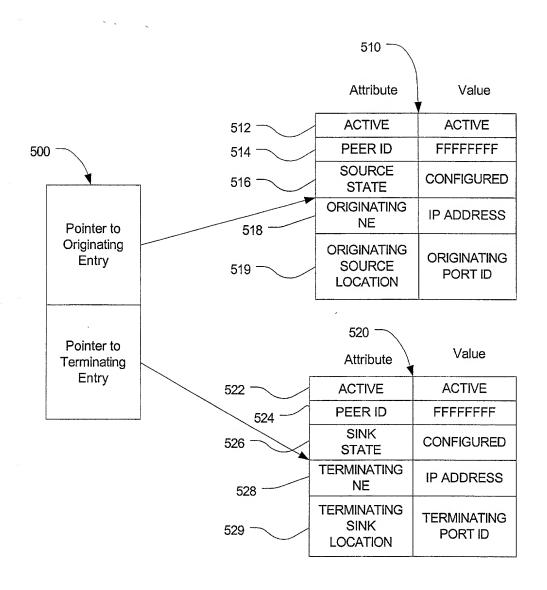


FIG. 14

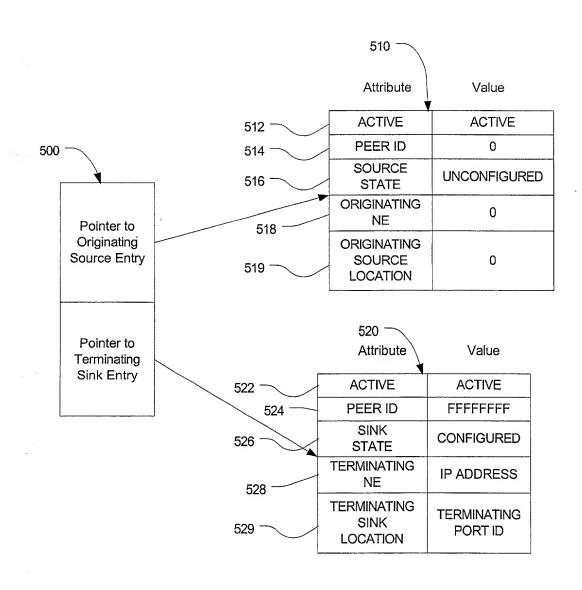


FIG. 15

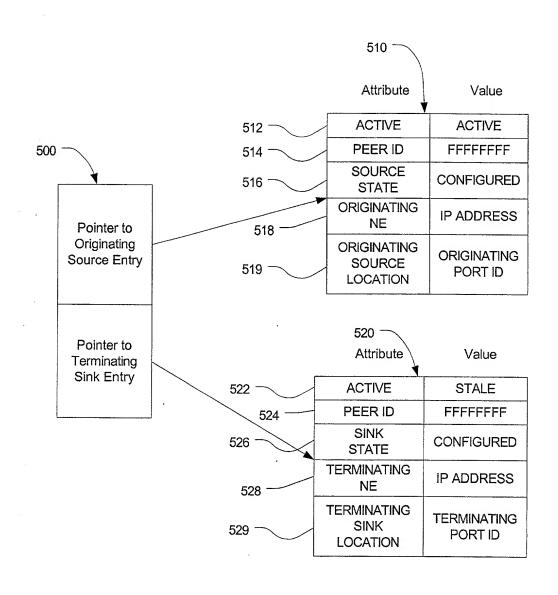


FIG. 16

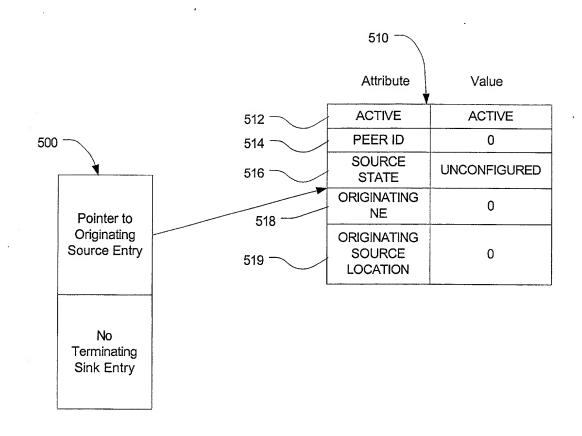


FIG. 17